

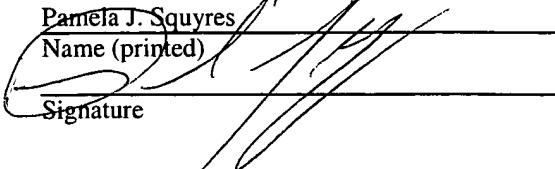


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Maitreyee Mahajani et al.

Application No.: 10/668693

Group Art Unit: 2811

Filed: September 23, 2003

Examiner: Unassigned

Title: Storage Layer Optimization of a Non

Volatile Memory Device

Attorney Docket No.: MA-105

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

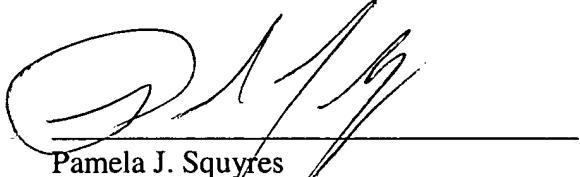
Dear Sir or Madam,

Pursuant to the obligation under 37 CFR § 1.56 and in conformance with 37 CFR §§ 1.97-1.99, Applicants hereby submit documents A1-A3 listed on the attached form PTO-1449 for consideration by the Examiner. Copies of the non-patent reference documents are enclosed herewith. Applicants request that the Examiner review the entire disclosure of these documents and make them of record.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 CFR §1.56(b). Further Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Dated: January 28, 2004

Respectfully submitted,



Pamela J. Squyres
Agent for Applicants
Reg. No. 52,246

Pamela J. Squyres
Matrix Semiconductor
3230 Scott Blvd
Santa Clara, CA 95054
Tel. 408-869-2921



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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | Complete if Known Application Number 10/668693 Filing Date September 23, 2003 First Named Inventor Mahajani, Maitreyee Group Art Unit 2811 Examiner Name Unknown | | | | | |
| Sheet 1 of 1 | | Attorney Docket No: MA-105 | | | | | |

US PATENT DOCUMENTS

| Examiner Initial * | Cite No | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
|--------------------|---------|---------------------|------------------|---|-------|----------|----------------------------|
| | A1 | 10/270127 | | Mahajani et al. | | | 10/15/2002 |

FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Cite No | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T ² |
|--------------------|---------|---------------------|------------------|---|-------|----------|----------------|
| | | | | | | | |

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

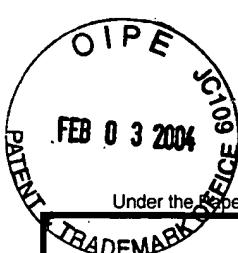
| Examiner Initials* | Cite No | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|--------------------|---------|---|----------------|
| | A2 | EVTUKH, A. A. ,et al. , "The Multilayer Silicon Nitride Films as a Media for Charge Storage in MNOS Structures", <u>1996 Int'l Nonvolatile Memory Technology Conference</u> , (4/96),pp. 91-93 | |
| | A3 | GOVOREANU, B. ,et al. , "VARIOT: A Novel Multilayer Tunnel Barrier Concept for Low-Voltage Nonvolatile Memory Devices", <u>IEEE Electron Device Letters</u> , 2003, (2003),pp. 1-3 | |

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached



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FORM**

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|--|----|------------------------|---------------------|
| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | | Application Number | 10/668693 |
| | | Filing Date | Sep 23, 2003 |
| | | First Named Inventor | Mahajani, Maitreyee |
| | | Art Unit | 2811 |
| | | Examiner Name | Unknown |
| Total Number of Pages in This Submission | 10 | Attorney Docket Number | MA-105 |

ENCLOSURES (check all that apply)

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| <input type="checkbox"/> Fee Transmittal Form | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> After Allowance communication to Group |
| <input type="checkbox"/> Fee Attached | <input type="checkbox"/> Licensing-related Papers | <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences |
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| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | | |

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

| | |
|-------------------------|--|
| Firm or Individual name | Matrix Semiconductor Pamela J. Squyres, Reg. No. 52,246 |
| Signature | |
| Date | January 28, 2004 |

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| Type or printed | Pamela J. Squyres | | |
| Signature | | Date | January 28, 2004 |

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.